# Adam DeConinck

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### Summary

Computer systems engineer with extensive experience designing, building, and operating cluster computing systems at scale for machine leaning and simulation workloads, as well as investigating and solving complex hardware and software issues in large distributed systems.

# **Professional Experience**

2023 – Present	<b>Senior Engineering Manager,</b> NVIDIA Corporation.
	<ul> <li>Manager of a team focused on designing next-generation systems for use by both internal teams and as a template for external deployments</li> <li>Develop cluster architectures based on novel hardware and software systems</li> <li>Future planning of datacenters and cluster systems for internal use at scale</li> </ul>
2022 – 2023	<b>Principal Systems Software Engineer,</b> NVIDIA Corporation.
	<ul> <li>Systems engineering and automation for large supercomputing systems, including NVIDIA Eos based on DGX H100 and NDR InfiniBand</li> <li>Investigate and diagnose issues on next generation hardware platforms</li> <li>Design new cluster architectures for AI and HPC applications at scale</li> </ul>
2021 – 2022	<b>Product Architect,</b> NVIDIA Corporation.
	<ul> <li>Subject-matter expert collaborating with engineering and product management</li> <li>Design of new system software stack for DGX SuperPOD AI supercomputers</li> <li>Worked closely with early SuperPOD customers to incorporate their feedback</li> </ul>
2019 – 2021	<b>Senior Solutions Architect,</b> NVIDIA Corporation.
	<ul> <li>Customer-facing system architect for supercomputing design and deployment</li> <li>Lead for multiple RFP responses for supercomputing systems at government customer sites, including deals with a total value of &gt;\$60MM.</li> <li>System software lead for initial customer deployments of DGX SuperPOD</li> </ul>
2017 – 2019	<b>Production Engineer,</b> Facebook, Inc.
	<ul> <li>Production Engineering tech lead for metadata layer of Facebook Tectonic, an exabyte-scale distributed filesystem deployed in multiple regions</li> <li>Deployment automation and automated capacity planning tools</li> <li>Onboarding and mentoring of new team members</li> <li>Helped organize multiple team events for Seattle production engineering team</li> </ul>
2014 – 2017	<b>HPC Cluster Administrator,</b> Los Alamos National Laboratory.
	<ul> <li>Systems engineer supporting HPC capability systems for major DOE workloads</li> <li>Production team lead for deploying Trinity, a 20,000-node Cray XC40 system</li> <li>Coordinated the production change control process for LANL HPC systems</li> <li>Derivative classifier to review the release of sensitive information (Q clearance)</li> </ul>

### **Professional Experience (continued)**

2012 – 2014	HPC/Cloud Systems Engineer, NVIDIA Corporation.				
	<ul> <li>Built and managed an HPC platform for the Solution Architect team</li> <li>This resource also supported customer evaluation workloads, as well HPC development for teams across the company.</li> </ul>				
2010 – 2012 Systems Engineer, R Systems NA, Inc.					
	• Design, deploy, and support hosted HPC clusters for industrial customers				
Technology Experience					
Programming languag	es Revealed Production code in Python, Go, Perl, Bash, and Fortran; Small tools and side projects in Rust, C++, and C				

	Sman tools and side projects in Rust, C++, and C
System orchestration	Ansible, Terraform, Cfengine
Workload management	Kubernetes, Slurm, PBS/Torque/Moab, Grid Engine
Cloud platforms	AWS, Azure, Equinix Metal
Storage technology	Lustre, Ceph, HDFS, Swift, MinIO; storage development at Facebook

## **Miscellaneous Experience**

#### **Open Source**

2019 – 2022	NVIDIA DeepOps, GPU cluster deployment toolkit for HPC and machine learning.
	Co-maintainer, extensive work on both Kubernetes and Slurm cluster deployment
2013 - 2014	<b>EasyBuild</b> , scientific software package manager. "Dry-run" functionality and added recipes
2011 - 2012	Warewulf, cluster provisioning system. IPMI functionality, misc bug fixes, beta tester for 3.0 release

### **Community Involvement**

- Program committee participant for several academic and industry conferences, including USENIX LISA, IEEE CLUSTER (for the HPCMASPA workshop), and the ACM SIGHPC-SYSPROS workshop.
- Regular participant in HPC community discussions and panels as part of ACM SIGHPC-SYSPROS.
- Member of the Denver Curling Club.

### Education

2007 - 2010	M.S., Univesity of Illinois at Urbana-Champaign, Materials Science and Engineering.
	Thesis title: Fabrication, dynamics and self-assembly of anisotropic colloidal particles.
2003 - 2007	B.S., Michigan Technological University, Physics.
	Minors in Mathematics and Electronic Materials.